



PATENT Customer No. 22,852 Attorney Docket No. 04329.2725-00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Atsushi SHIMBO et al.) Group Art Unit: 2135
Serial No.: 10/051,276) Examiner: Nirav B. PATEL
Filed: January 22, 2002) Confirmation No.: 7600
For: MODULAR EXPONENTIATION CALCULATION APPARATUS AND MODULAR EXPONENTIATION CALCULATION METHOD))

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(c)

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(c), Applicants bring to the attention of the Examiner the documents on the attached listing. This Information Disclosure Statement is being filed after the events recited in Section 1.97(b), but to the undersigned's knowledge, before the mailing date of either a Final action, Quayle action, or a Notice of Allowance. Under the provisions of 37 C.F.R. § 1.97(c), this Information Disclosure Statement includes a certification as specified by Section 1.97(e).

Each document listed in this Information Disclosure Statement was first cited in a communication from the Japanese Patent Office in a counterpart foreign application, and this Information Disclosure Statement is being filed within three months of the mailing date of that communication.

Applicants respectfully request that the Examiner consider the listed documents and indicate that they were considered by making appropriate notations on the attached form.

Copies of the listed foreign and non-patent literature documents are attached.

Customer No. 22,852 Application Serial No. 10/051,276

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In lieu of a statement of relevance or translation of the non-English document, an

English language version of a communication from the Japanese Patent Office in a

corresponding application citing the document and setting forth the relevance thereof is

enclosed.

This submission does not represent that a search has been made or that no better art

exists and does not constitute an admission that each or all of the listed documents are material

or constitute "prior art." If the Examiner applies any of the documents as prior art against any

claim in the application and Applicants determine that the cited documents do not constitute

"prior art" under United States law, Applicants reserve the right to present to the office the

relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the

patentability of the disclosed invention over the listed documents, should one or more of the

Bv:

documents be applied against the claims of the present Application.

If there is any fee due in connection with the filing of this Statement, please charge the

fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,

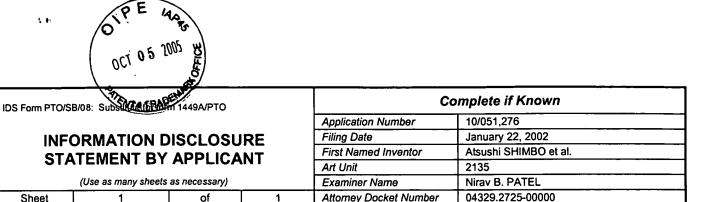
GARRETT & DUNNER, L.L.P.

Dated: October 5, 2005

Kenie Ho

Reg. No. 51,808

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Attorney Docket Number

U.S. PATENTS AND PUBLISHED U.S. PATENT APPLICATIONS						
Examiner Initials	Cite No. ¹	Document Number	Issue or Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where	
		Number-Kind Code ² (if known)			Relevant Passages or Relevant Figures Appear	

Note: Submission of copies of U.S. Patents and published U.S. Patent Applications is not required.

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FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Translation
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Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Translation ⁶
	Masanobu Koike al., "Cox-Rower Architecture for Fast Parallel Montgomery Multiplication," The 2000 Symposium on Cryptography and Information Security, January 27, 2000, B22.	No
	Khaled M. Elleithy et al., "Fast and Flexible Architectures for RNS Arithmetic Decoding," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, April 1992, Vol. 39, No. 4, Pages 226-235.	N/A
	Notification of Reasons for Rejection dated September 6, 2005, from the Japanese Patent Office for Japanese Patent Application No. 2001-013565.	Yes
_	Separation value in Application viol. 2001 of 100000.	
		No. (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. Masanobu Koike al., "Cox-Rower Architecture for Fast Parallel Montgomery Multiplication," The 2000 Symposium on Cryptography and Information Security, January 27, 2000, B22. Khaled M. Elleithy et al., "Fast and Flexible Architectures for RNS Arithmetic Decoding," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, April 1992, Vol. 39, No. 4, Pages 226-235. Notification of Reasons for Rejection dated September 6, 2005, from the Japanese Patent Office for

Examiner	Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.